

Fig. 1

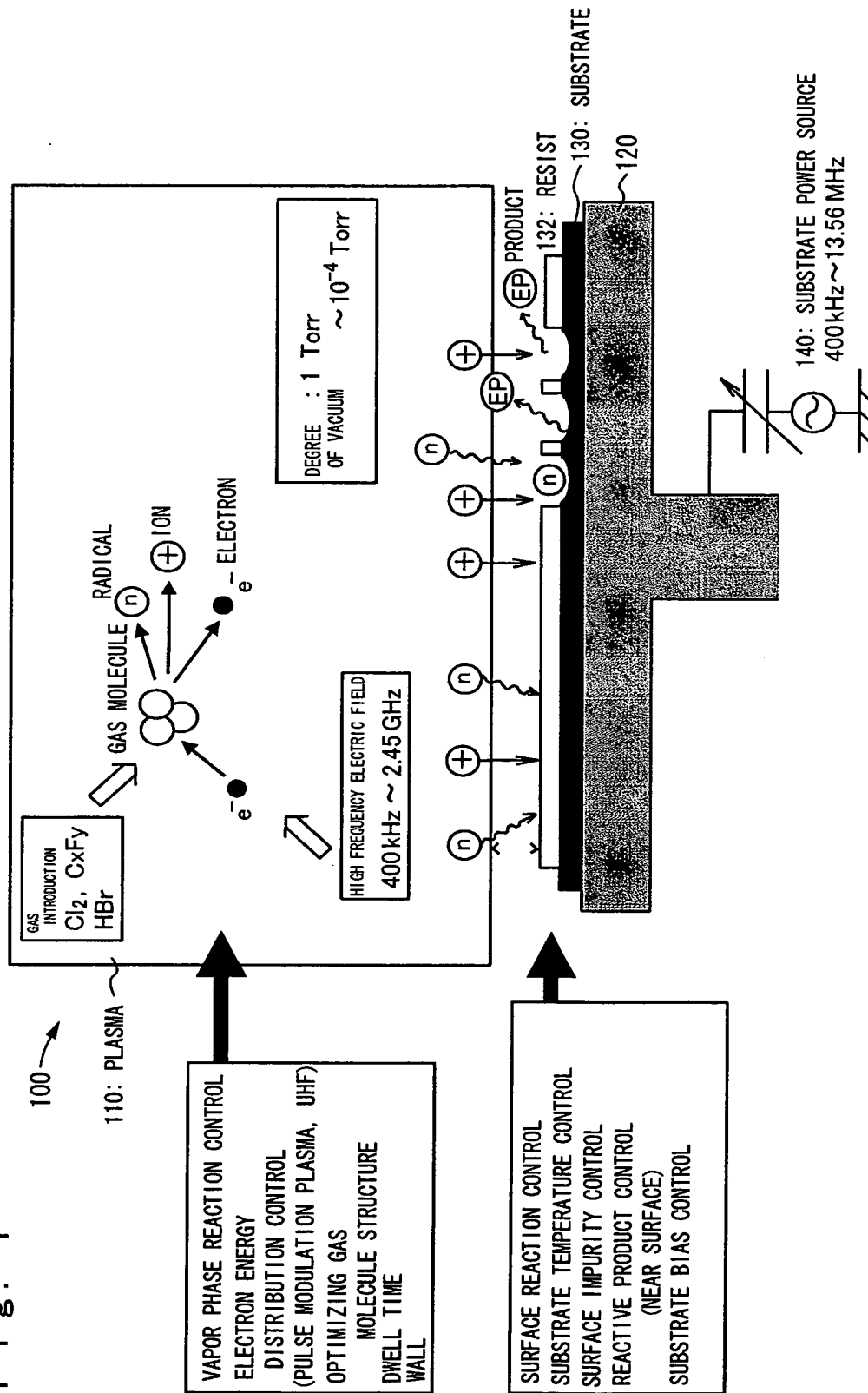


Fig. 2

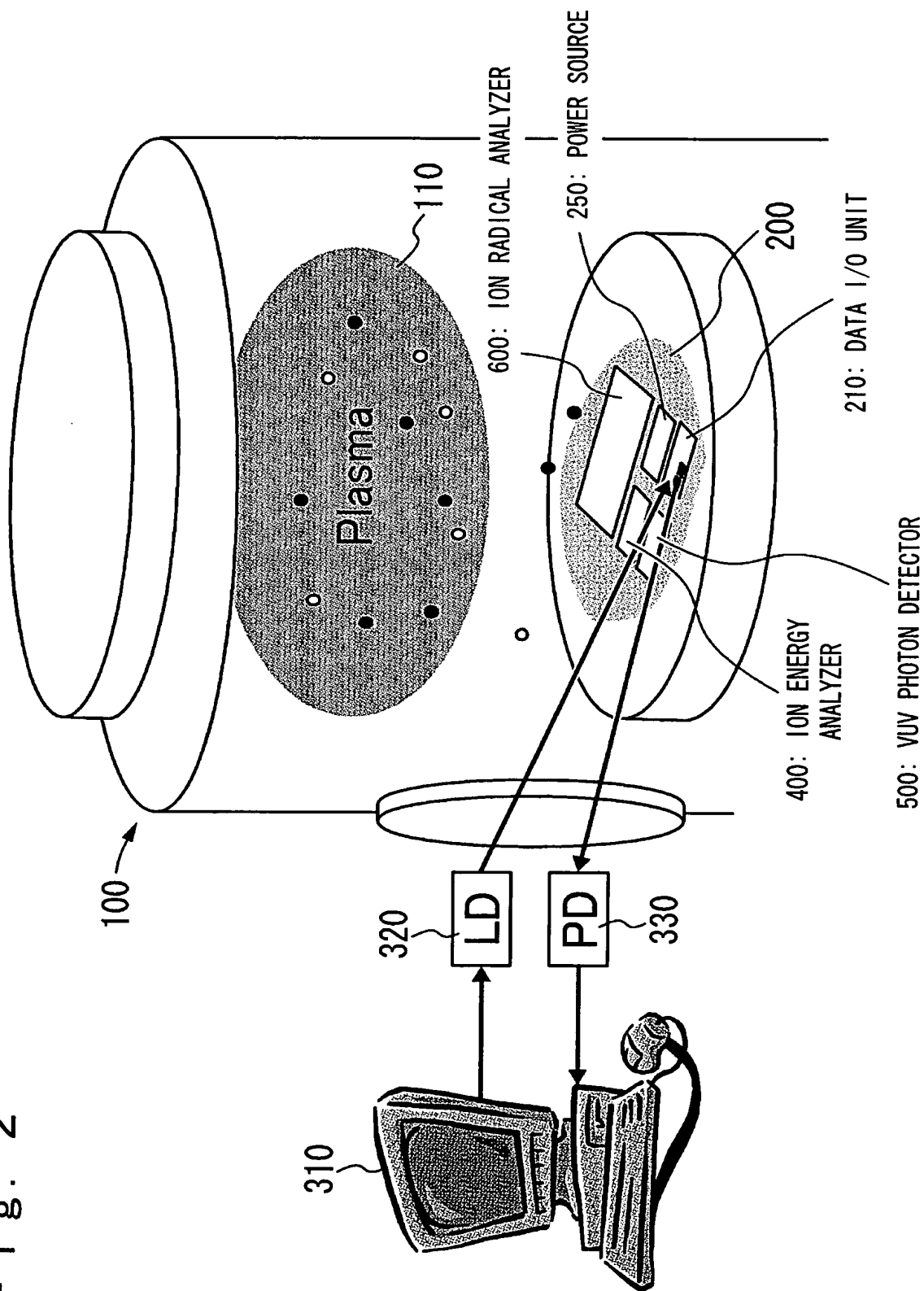


Fig. 3

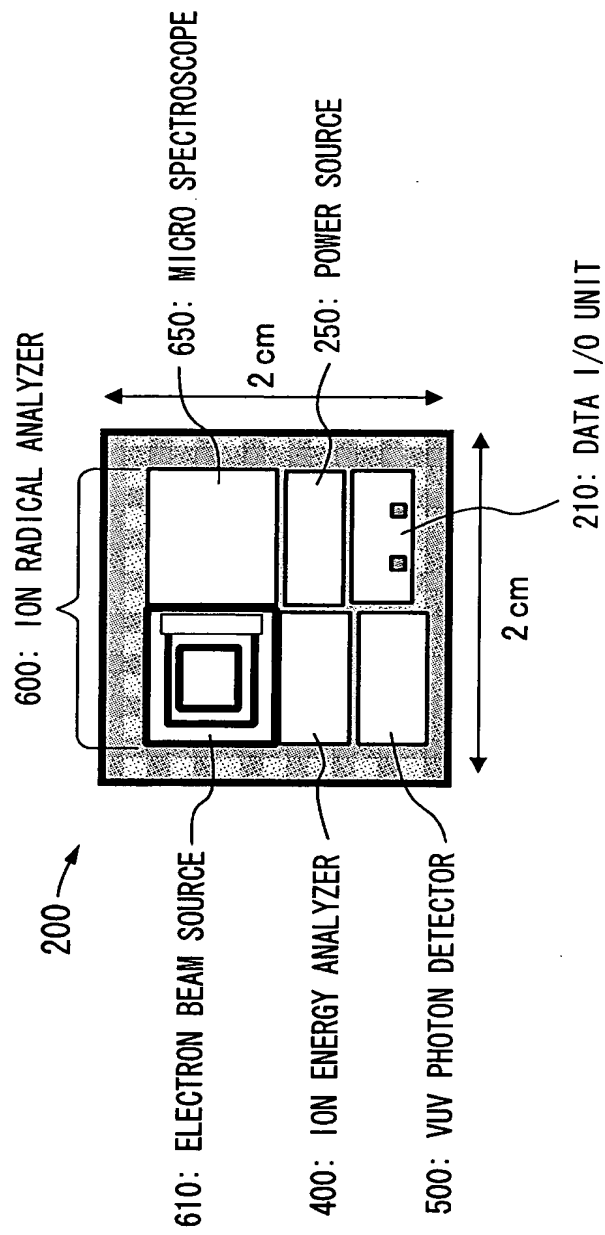


Fig. 4

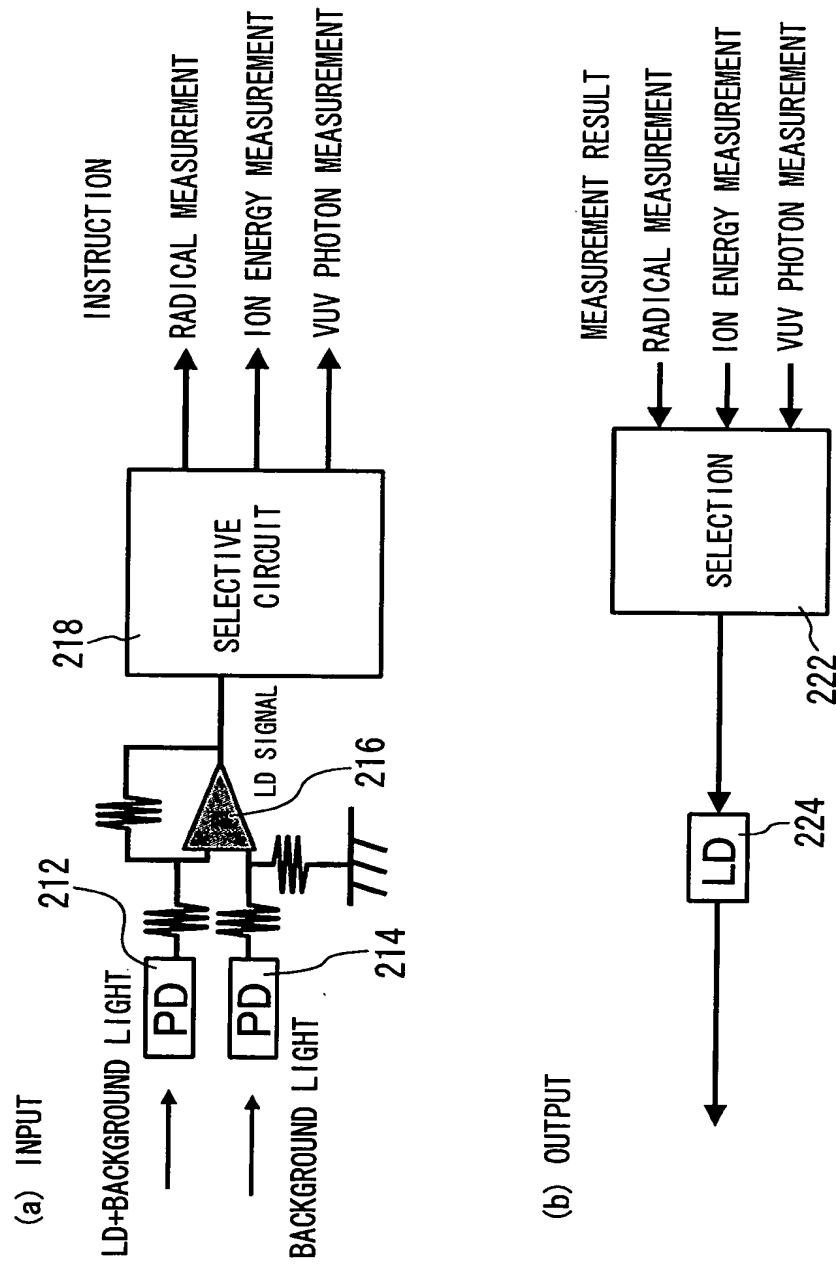


Fig. 5

POTENTIAL DIFFERENCE BETWEEN PLASMA SPACE POTENTIAL  
AND GROUND IS UTILIZED.

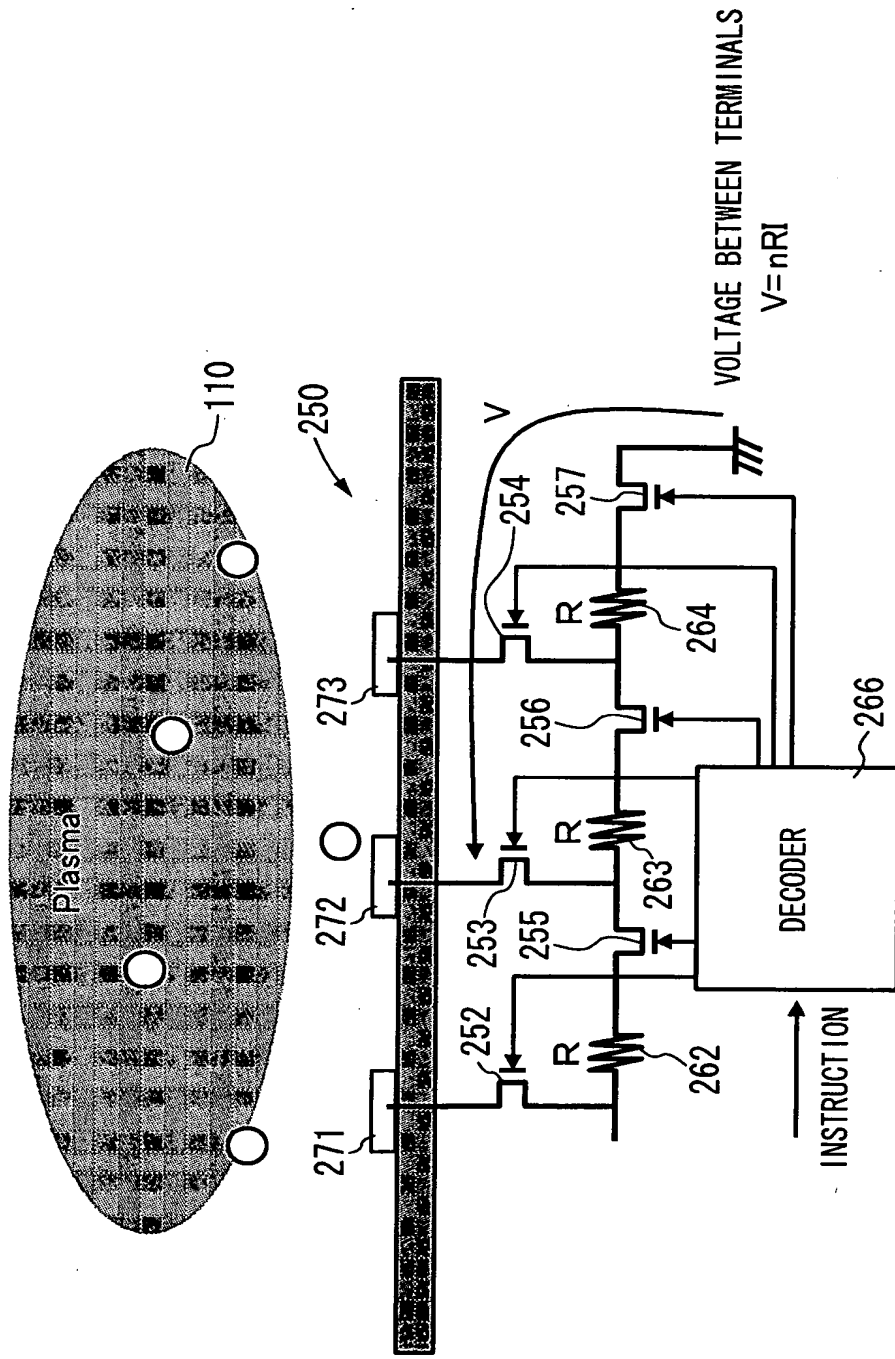
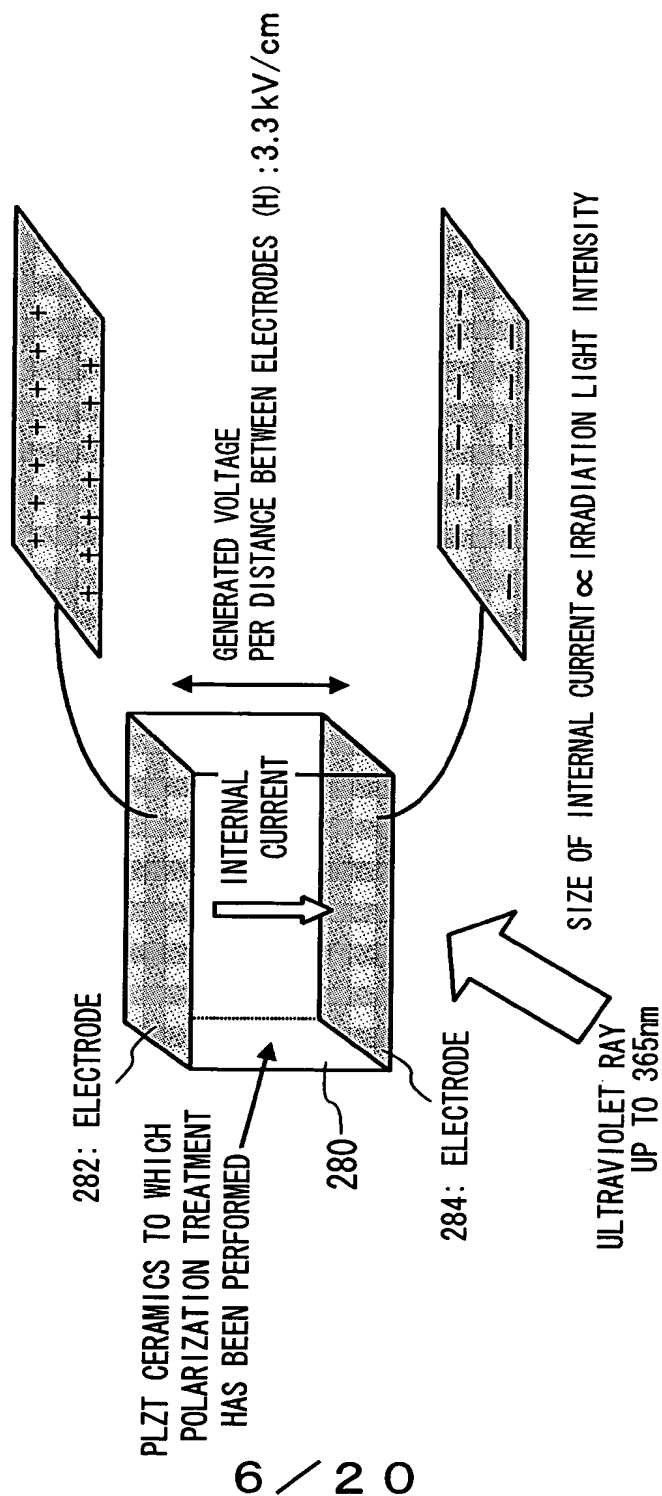


Fig. 6



F i g . 7

## STRUCTURE OF ON-WAFER ION ENERGY ANALYZER

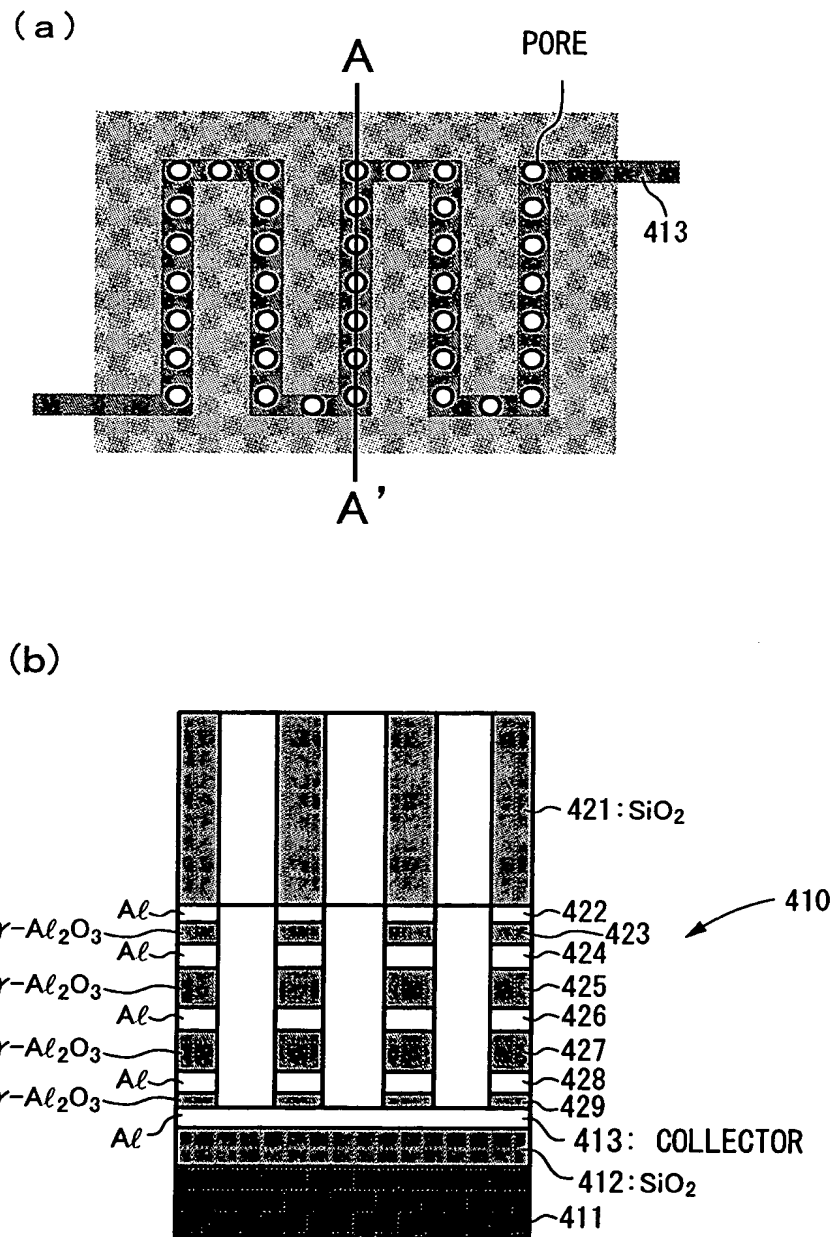
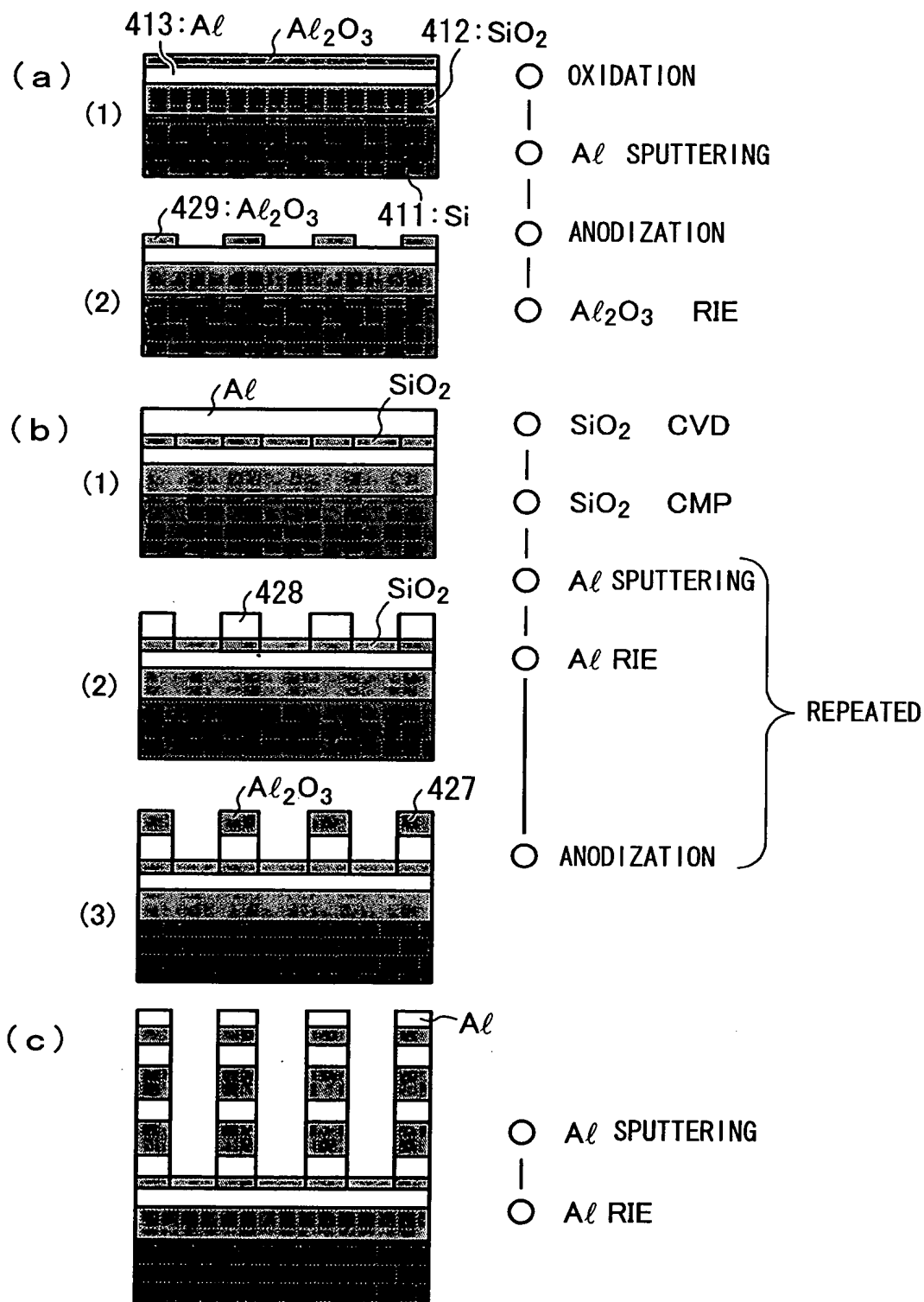






Fig. 9



F i g . 1 0

(d)

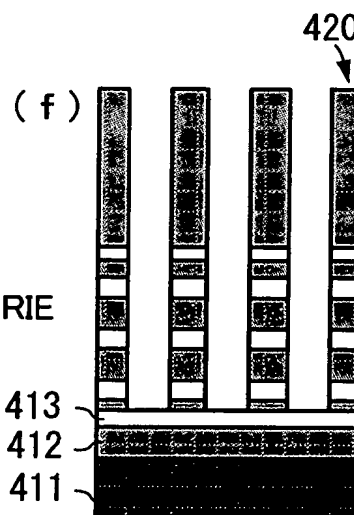
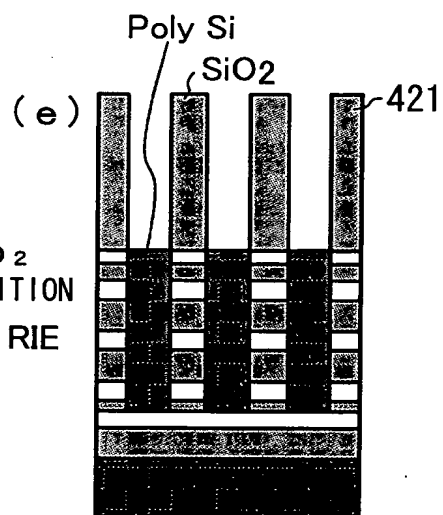
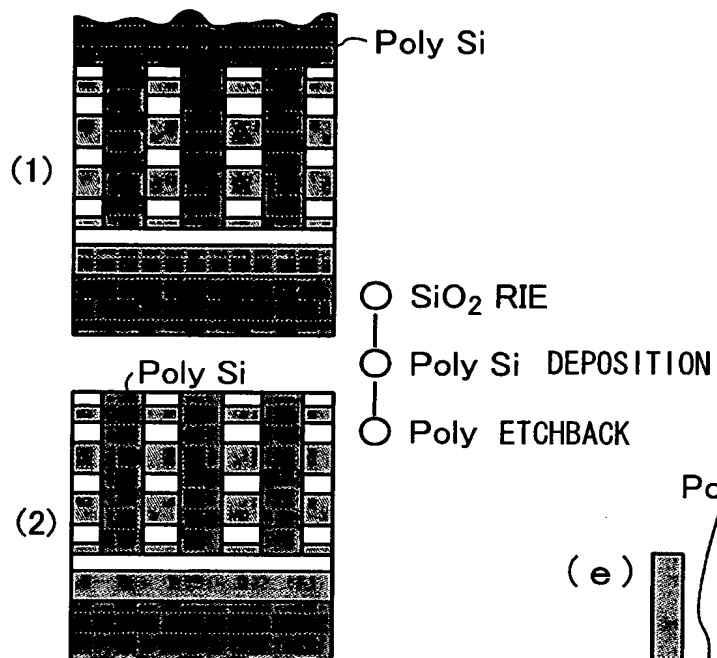
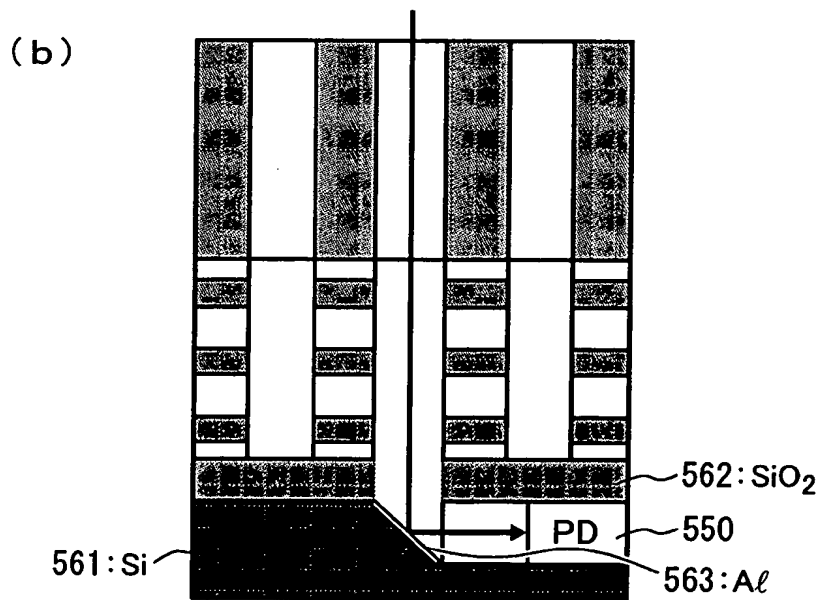
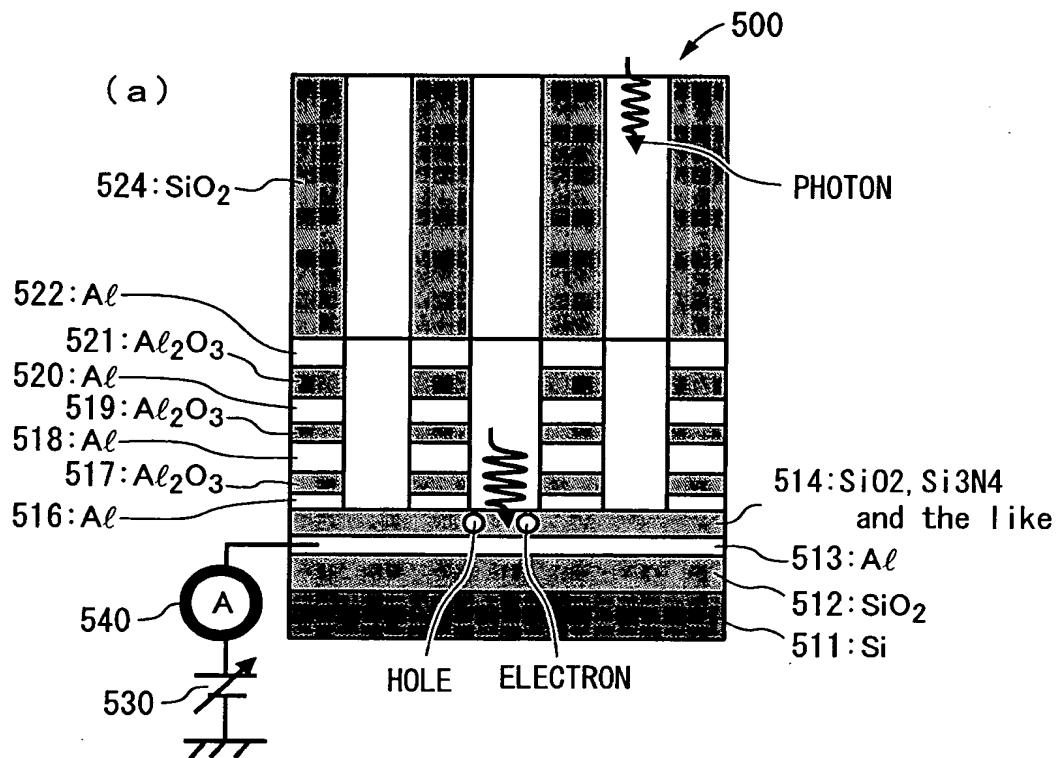


Fig. 11



Fi 50.12

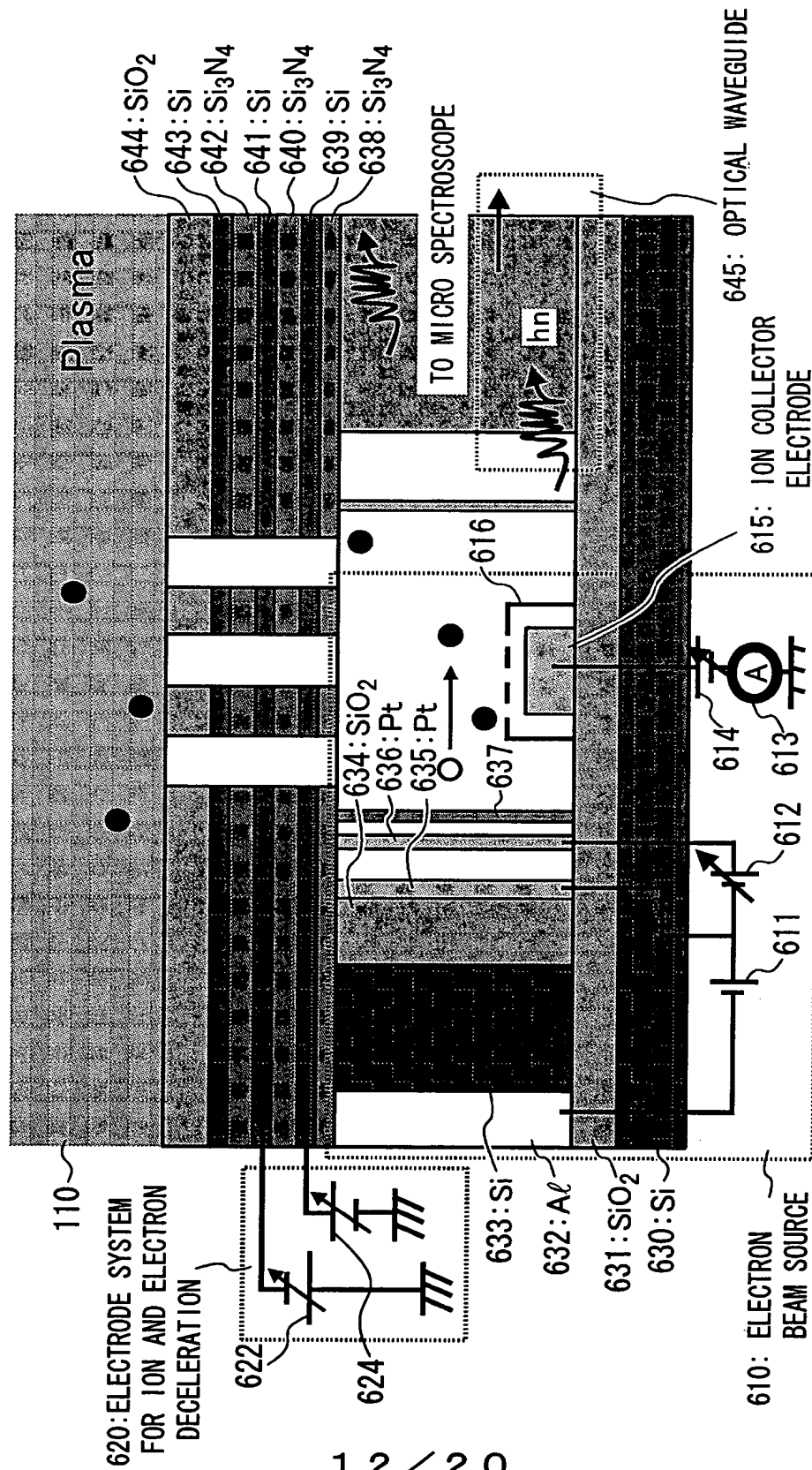


Fig. 13

## FIELD EMISSION TYPE ELECTRON GUN

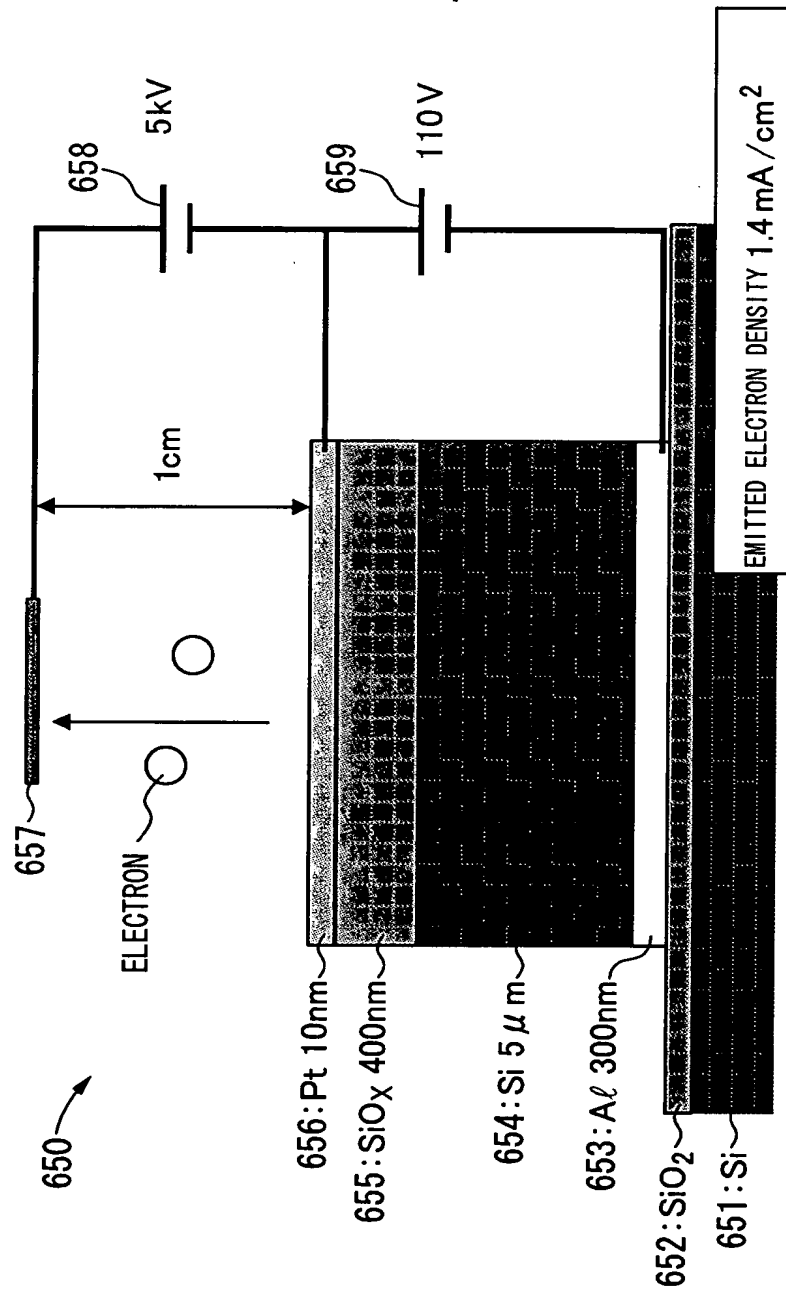


Fig. 14

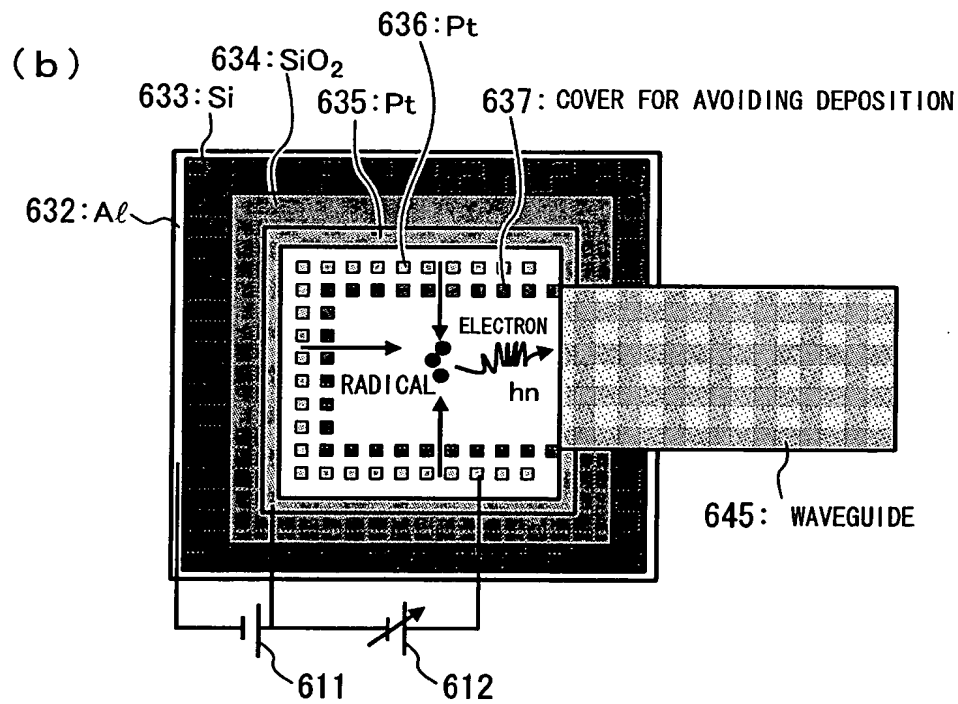
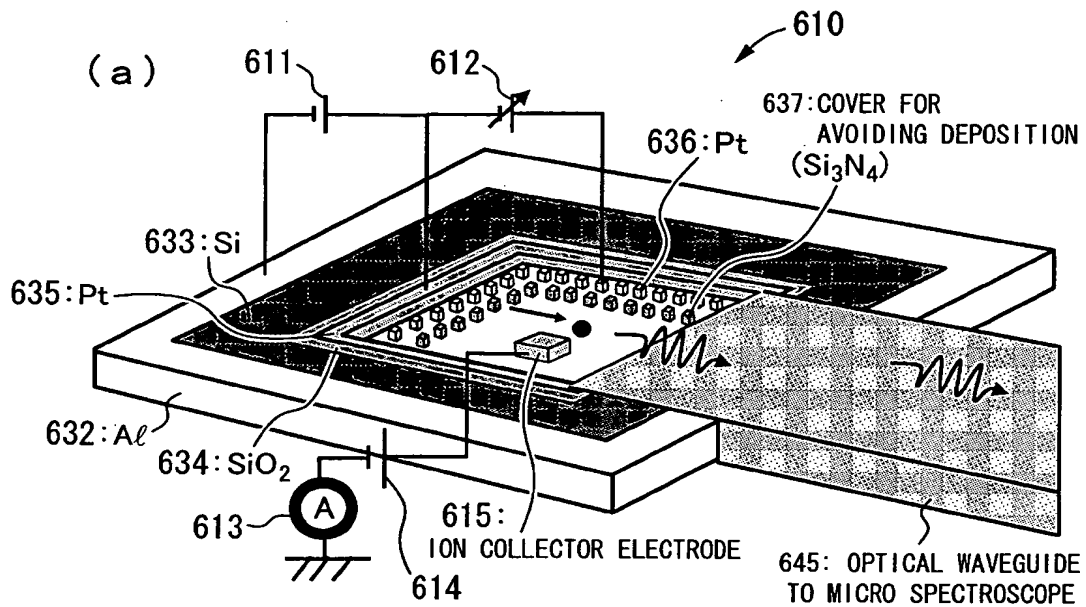


Fig. 15

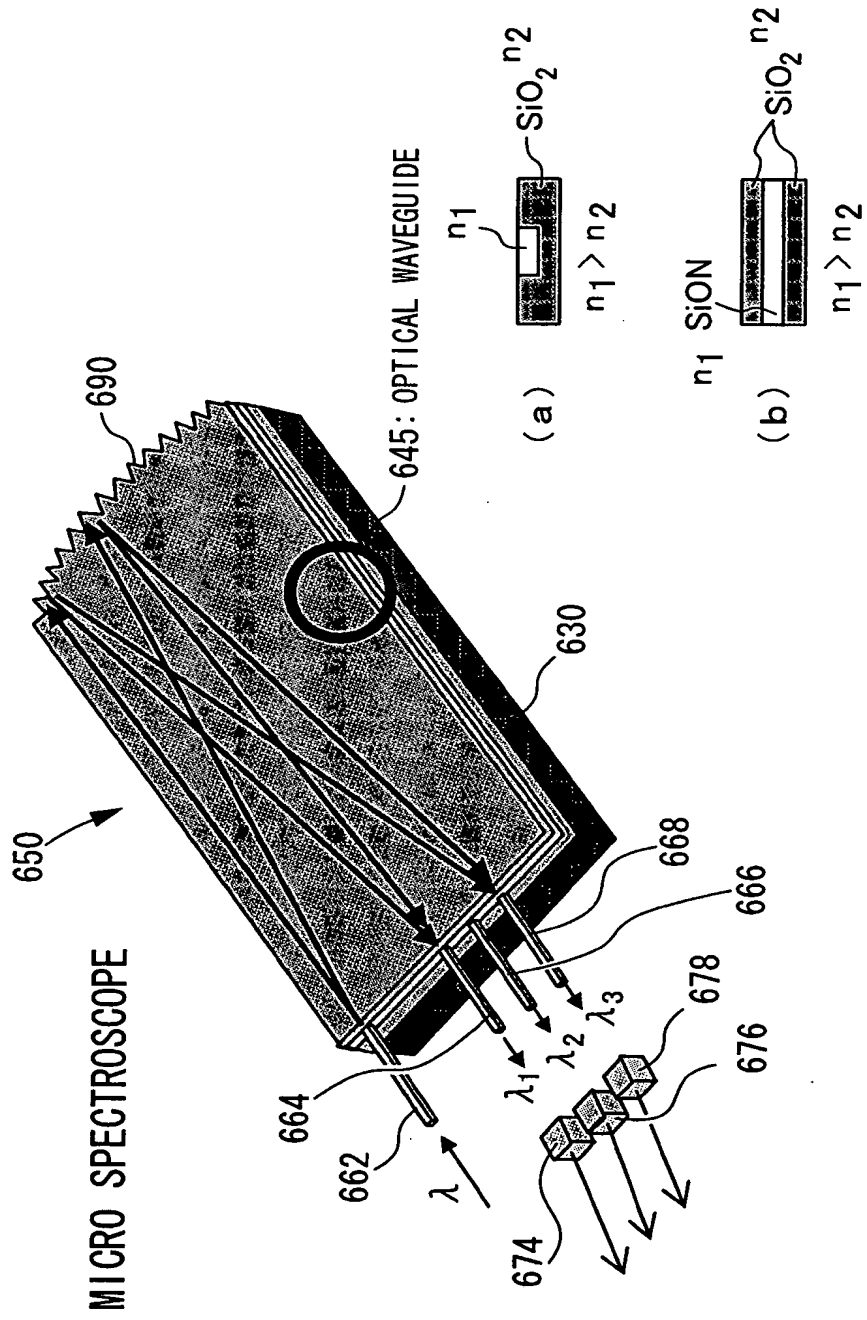


Fig. 16

## MANUFACTURING PROCESS OF MICRO ION RADICAL ANALYZER(1)

(a) OXIDATION



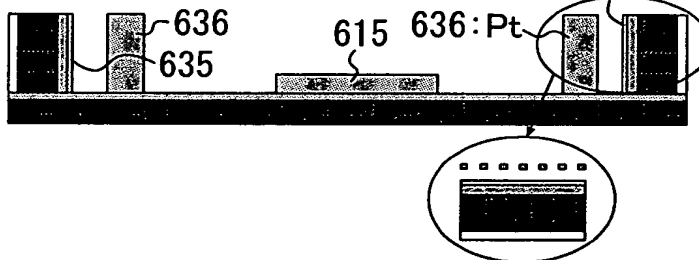
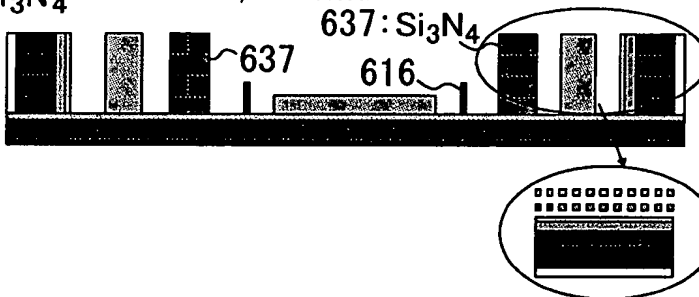
(b) Si DEPOSITION, ETCHING



(c) Al VAPOR DEPOSITION, ETCHING

(d)  $\text{SiO}_2$  DEPOSITION, ETCHING

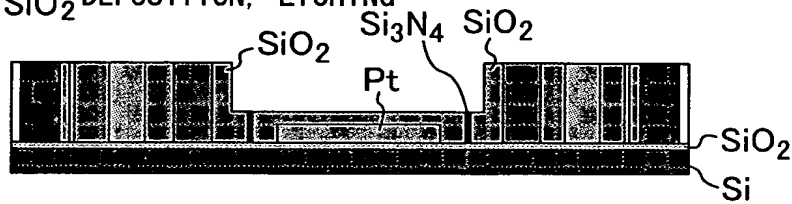
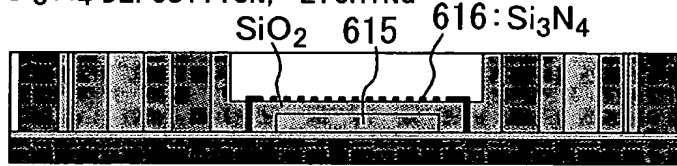
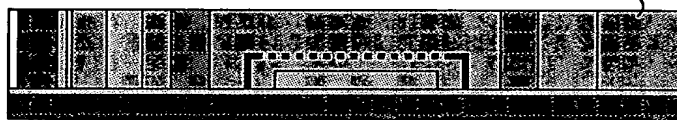
(e) Pt DEPOSITION, PATTERNING

(f)  $\text{Si}_3\text{N}_4$  DEPOSITION, ETCHING



F i g. 17

## MANUFACTURING PROCESS OF MICRO ION RADICAL ANALYZER (2)

(g)  $\text{SiO}_2$  DEPOSITION, ETCHING(h)  $\text{Si}_3\text{N}_4$  DEPOSITION, ETCHING(i)  $\text{SiO}_2$  DEPOSITION, ETCHING  
FORMATION OF WAVEGUIDE

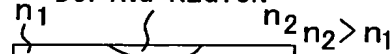
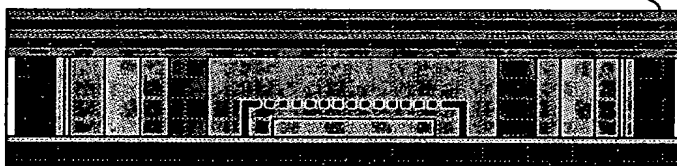
645: ION DOPING

ELECTRON BEAM SOURCE



WAVEGUIDE

DOPING REGION

(j)  $\text{Si}_3\text{N}_4$ , Si DEPOSITION643:  $\text{Si}_3\text{N}_4$ 

642: Si

641:  $\text{Si}_3\text{N}_4$ 

640: Si

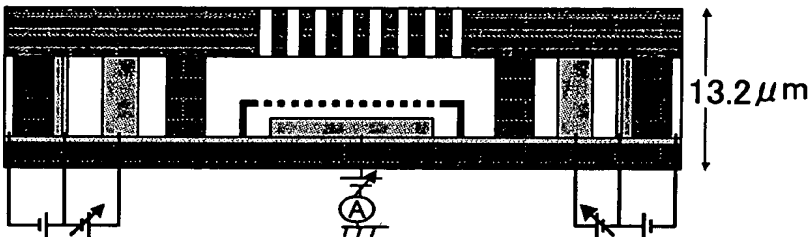
649:  $\text{Si}_3\text{N}_4$ (k)  $\text{Si}_3\text{N}_4$ , Si ETCHING  
 $\text{SiO}_2$  SACRIFICE LAYER ETCHING

Fig. 18

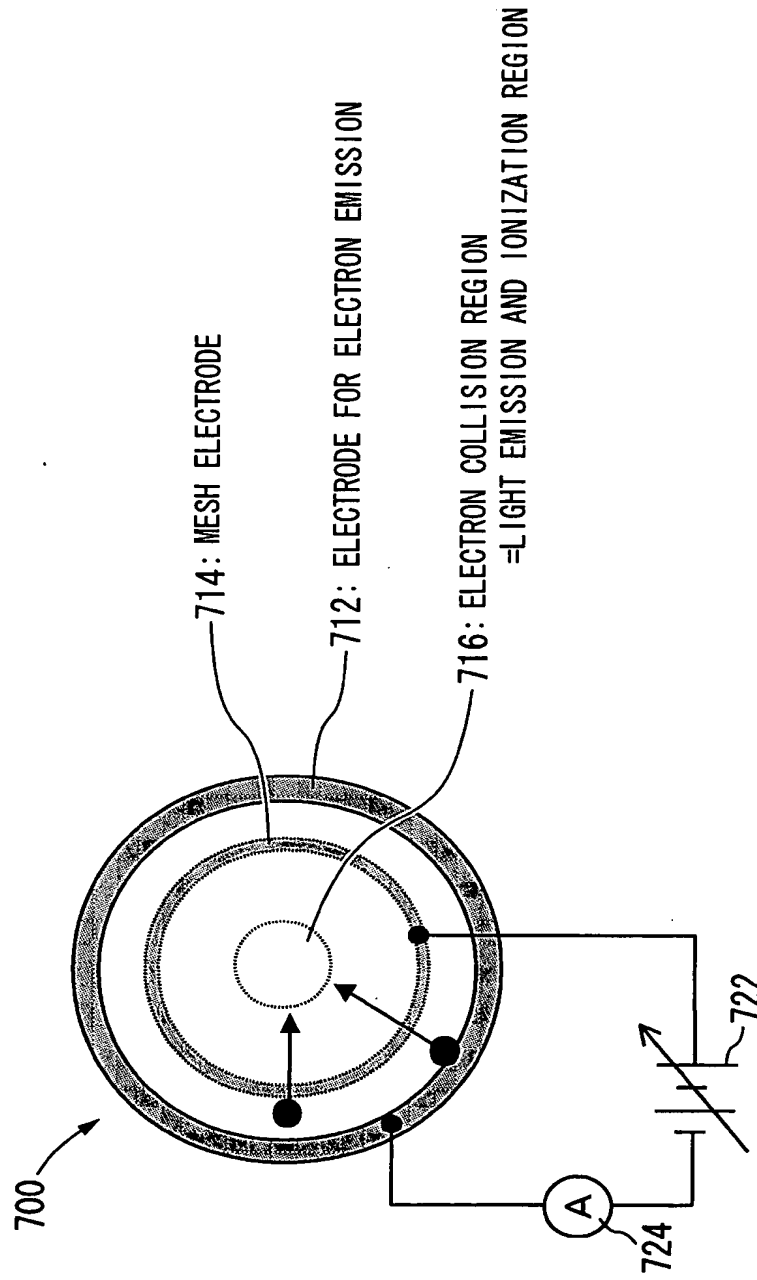


Fig. 19

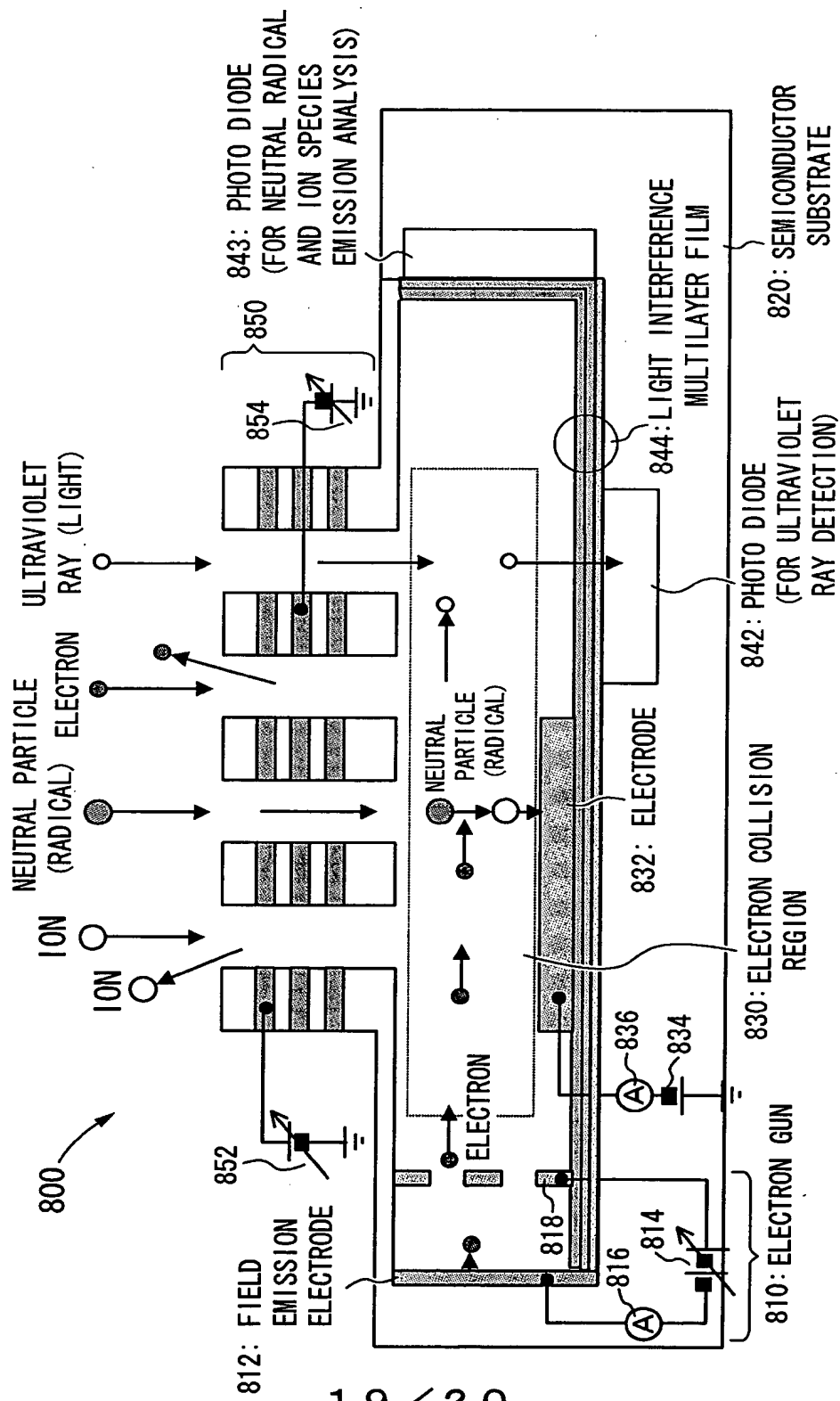


Fig. 20

